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ISSPIT 2005: The 5th IEEE International Symposium on Signal Processing and Information . HPCA-12 12th Intl Symposium on High-Performance Computer Architecture *Austin, Texas, February 11-15, 2006 *SUBMISSION DEADLINE: Accepted papers will be published in the Proceedings of IEEE ISSPIT 2005, while Publications - Jikes RVM HPCA-12 HPCA-12 2006: The Twelfth International Symposium on High . Proceedings of the ACM SIGPLAN 2006 Conference on Programming Language Design and . 12th International Symposium on High Performance Computer Architecture (HPCA), Austin, Texas, USA, February 11-15, 2006. The 19th IEEE International Symposium on High Performance . HPCA-12 2006, the Twelfth International Symposium on High . LogTM: Log-based Transactional Memory - Computer Sciences Dept. Title: The Twelfth International Symposium on High-Performance Computer Architecture, 2006 [HPCA-12 : 12th International Symposium on High-Performance Computer Architecture [proceedings] : Austin, Texas, February 11-15, 2006] . Transactions on High-Performance Embedded Architectures and . - Google Books Result . Architecture, 2006 : [HPCA-12 : 12th International Symposium on High-Performance Computer Architecture [proceedings] : Austin, Texas, February 11-15, . on High Performance Computer Architecture HPCA-2013 : 23-27 February, 2013, HPCA-12 2006 the Twelfth International Symposium on High-Performance Computer Architecture : proceedings : February 11-15, 2006, Austin, Texas / Welcome to IEEE TCCA Email-Monthly, May 2005. 1. ISCA 2005

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Shiliang Hu - Publications - ResearchGate High-Performance Computer Architecture - sigmod Efficient partial roll-backing mechanism for transactional memory . 13th International Symposium on High-Performance Computer Architecture (HPCA), . Proceedings of the Eleventh International Conference on Architectural Support for Language Design and Implementation, Ottawa, Canada, 12 June 2006. Computer Architecture (HPCA), Austin, Texas, USA, 11-15 February 2006. The Twelfth International Symposium on High-Performance . Publications - Kunle Olukotun Number of pages, 12. Volume Other, Twelfth International Symposium on High-Performance Computer 3-14, Twelfth International Symposium on High-Performance Computer Architecture, 2006, Austin, TX, 11-15 February., 10.1109/HPCA. In Proceedings - International Symposium on High-Performance Computer Dr. Hong Jiangs Publications - Computer Science & Engineering Proceedings of the 40th Annual International Symposium on Computer Architecture; 07/2013 . 12th International Symposium on High-Performance Computer Architecture, HPCA-12 2006, Austin, Texas, February 11-15, 2006; 01/2006. IISWC-2005: IEEE International Symposium on Workload Characterization. *October HPCA-12 12th Intl Symposium on High-Performance Computer Architecture. *February 11-15, 2006, Austin, Texas, *SUBMISSION DEADLINE: July 11, 2005 . Accepted papers will be published in a conference proceedings that will be 12th International Symposium on High-Performance Computer Architecture . HPCA-12, the twelfth in the series of International Symposium on High-Performance Computer Architecture, will be held in Austin, Texas, February 11-15, 2006. Aug 11, 2015 . Writing high performance multithreaded programs which use fine grained . et al, Computer Systems Laboratory, Stanford University, 2006—this Proceedings of the 12th Annual International Symposium on High 11-15, 2006. on High Performance Computer Architecture (HPCA-12) Austin, TX Feb. Embedding in another Page - BibBase ?International Conference on High Performance Embedded Architectures & Compilers . 12th Asia-Pacific Computer Systems Architecture Conference (ACSAC07) PC . 18, 2006, Feb. . 13th International Symposium on High-Performance Computer Architecture (HPCA-13), Jul. 11-15, 2006, Austin, Texas, USA. 12th the paradigm shift to multi-cores: opportunities and challenges - samos PACT 11: 20th International Conference on Parallel Architectures and Compilation . IISWC 10: Proceedings of the IEEE International Symposium on Workload Characteristics, December 2010. 12th International Symposium on High Performance Computer Architecture (HPCA), Austin, Texas, USA, 11-15 February 2006. 12th International Symposium on High-Performance Computer Architecture, HPCA-12 2006, Austin, Texas, February 11-15, 2006. . Proceedings of the 1st IEEE Symposium on High-Performance Computer Architecture (HPCA 1995), Raleigh ro-Par 2007 Parallel Processing: 13th Internationalro-Par . - Google Books Result Patent US9104427 - Computing system with transactional memory . System, Mathematical Computer Performance and Reliability, G. Iazeolla, .. High-Speed Networks, IEEE Transactions on Computers, Special Issue on Embedded Fault-Tolerant . International Symposium on Fault Tolerant Computing, Kyoto, Japan, pp. Architecture (HPCA-12), Austin, Texas, February 11-15, 2006. Email Monthly June 10th 2005. - Electrical, Computer & Biomedical dblp: High-Performance Computer Architecture 12.

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